

Claims

- [c1] 1. An electronic control device for controlling a controlled device installed on a bicycle, wherein the control device comprises:
 - a programmed computer that controls the control device; and
 - a reset circuit that receives information related to a traveling condition of the bicycle and provides a reset signal to the computer in response to the occurrence of a predetermined traveling condition.
- [c2] 2. The device according to claim 1 wherein the traveling condition comprises bicycle speed.
- [c3] 3. The device according to claim 2 wherein the predetermined traveling condition comprises the bicycle speed being below a predetermined value.
- [c4] 4. The device according to claim 1 wherein the traveling condition comprises a signal output by an alternating current generator.
- [c5] 5. The device according to claim 4 wherein the traveling condition comprises a frequency output by the alternating current generator.

- [c6] 6. The device according to claim 5 wherein the predetermined traveling condition comprises the frequency output by the alternating current generator being below a predetermined value.
- [c7] 7. The device according to claim 4 wherein the traveling condition comprises a voltage output by an alternating current generator.
- [c8] 8. The device according to claim 7 wherein the predetermined traveling condition comprises the voltage output by the alternating current generator being below a predetermined value.
- [c9] 9. The device according to claim 1 wherein the reset circuit provides a reset warning signal prior to providing the reset signal.
- [c10] 10. The device according to claim 9 wherein the reset circuit provides the reset signal a predetermined time interval after providing the reset warning signal.
- [c11] 11. The device according to claim 9 further comprising a memory coupled to the computer, and wherein the computer stores operating information in the memory in response to the reset warning signal.
- [c12] 12. The device according to claim 11 wherein the mem-

ory comprises a nonvolatile memory.

- [c13] 13. The device according to claim 9 wherein the reset circuit switches a power signal to the computer to a pre-determined state in response to the occurrence of the predetermined traveling condition.
- [c14] 14. The device according to claim 13 wherein the reset signal and the power signal comprise separate signals.
- [c15] 15. The device according to claim 13 wherein the reset circuit switches the power signal off in response to the occurrence of the predetermined traveling condition.
- [c16] 16. The device according to claim 1 wherein the reset circuit switches a power signal to the computer to a pre-determined state in response to the occurrence of the predetermined traveling condition.
- [c17] 17. The device according to claim 16 wherein the reset circuit switches the power signal off in response to the occurrence of the predetermined traveling condition.
- [c18] 18. The device according to claim 16 wherein the reset signal and the power signal comprise separate signals.
- [c19] 19. The device according to claim 16 wherein the power signal comprises a direct current signal.

- [c20] 20. The device according to claim 19 further comprising a rectifier for rectifying an alternating current signal from a dynamo installed on the bicycle into the direct current signal.
- [c21] 21. The device according to claim 1 wherein the reset circuit comprises:
 - a reset signal output circuit; and
 - a reset activating circuit that activates the reset signal output circuit in response to the occurrence of the pre-determined traveling condition.
- [c22] 22. The device according to claim 21 wherein the reset activating circuit comprises a power switching circuit that switches power to the reset activating circuit to a predetermined state in response to the occurrence of the predetermined traveling condition.
- [c23] 23. The device according to claim 22 wherein the reset circuit provides a reset warning signal prior to providing the reset signal.
- [c24] 24. The device according to claim 22 wherein the reset activating circuit switches a power signal off to the reset activating circuit in response to the occurrence of the predetermined traveling condition.
- [c25] 25. The device according to claim 22 wherein the reset

activating circuit switches a power signal off to the computer in response to the occurrence of the predetermined traveling condition.

- [c26] 26. The device according to claim 22 wherein the reset activating circuit switches the power signal off to the reset activating circuit and to the computer in response to the occurrence of the predetermined traveling condition.
- [c27] 27. The device according to claim 26 wherein the reset circuit provides a reset warning signal to the computer prior to providing the reset signal.